

A hand-drawn schematic diagram illustrating a system architecture. The diagram is divided into several labeled components:

- 116**: A rectangular block at the top left, filled with a pattern of small circles, representing a data source or storage.
- 122**: A smaller rectangular block within 116, also containing a circle pattern.
- 114**: A line connecting 116 to the main processing area.
- 112**: A large, irregularly shaped area on the right, representing a main processing or output stage.
- 115**: A dashed line with arrows pointing from 116 towards the main area 112.
- 128**: A rectangular block within the main area 112, connected to 116 by a line.
- 126**: A rectangular block below 128, connected to it by a line.
- 124**: A rectangular block below 126, connected to it by a line.
- 118**: A dashed line with arrows pointing from 128 towards the main area 112.
- 132a**, **132b**, and **132c**: Three wavy lines along the bottom and right edges of the main area 112, representing output or data flow.

Fig 1 - Prior Art

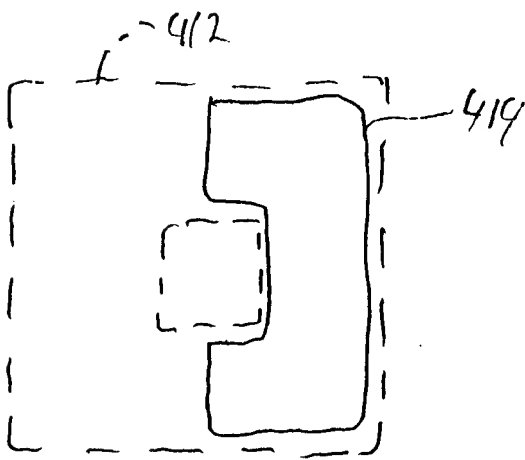
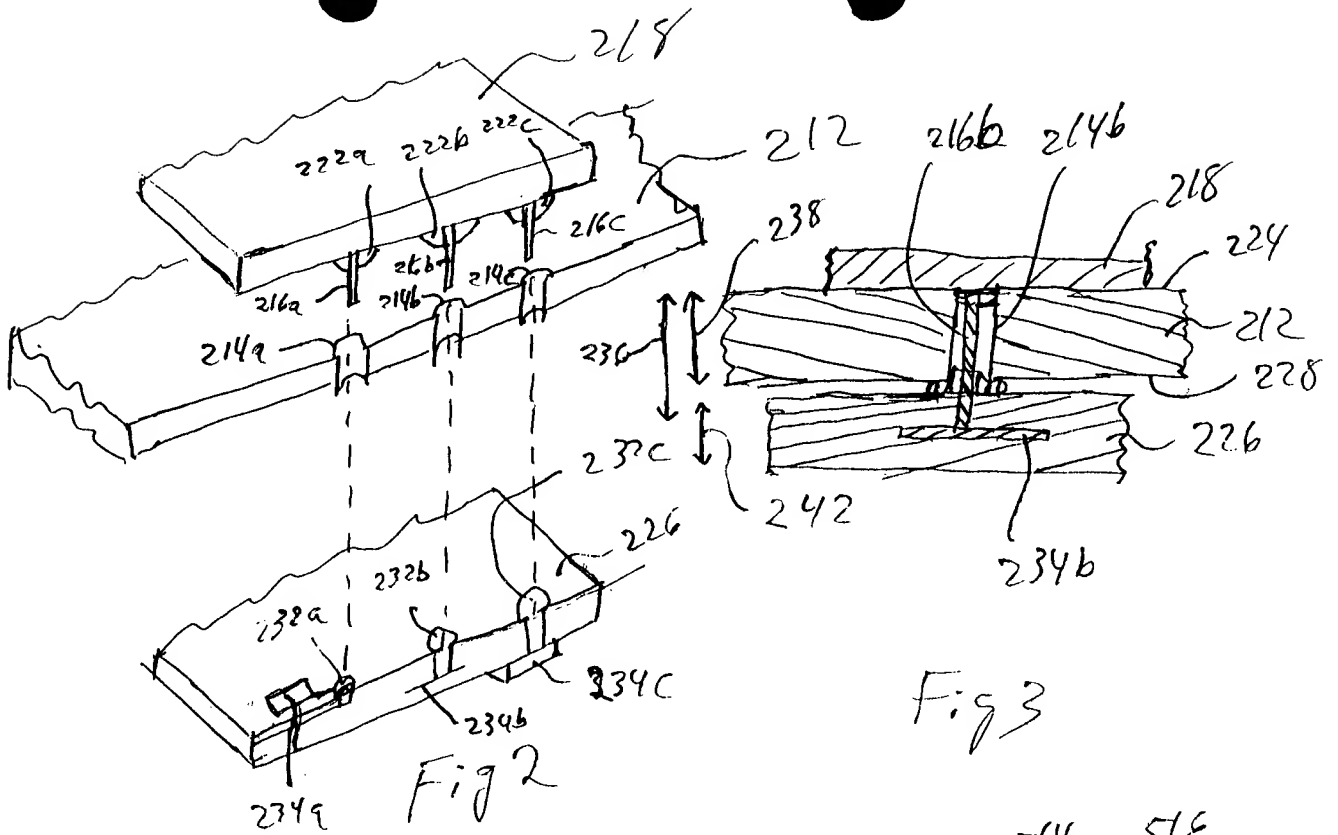


Fig 4

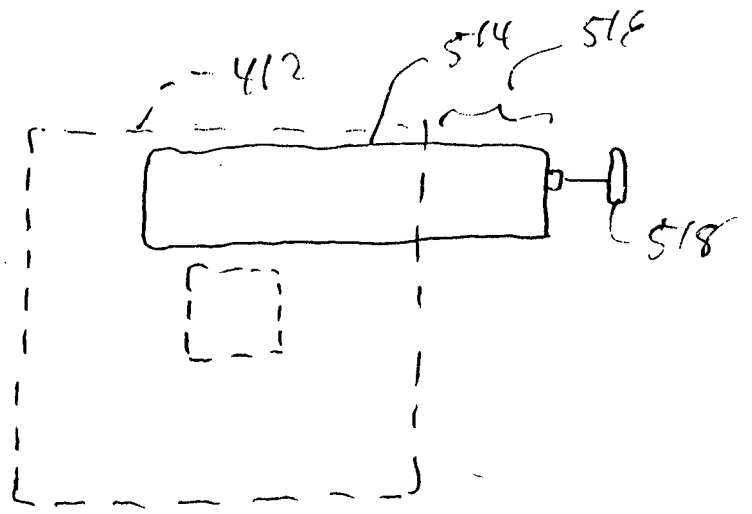


Fig 5

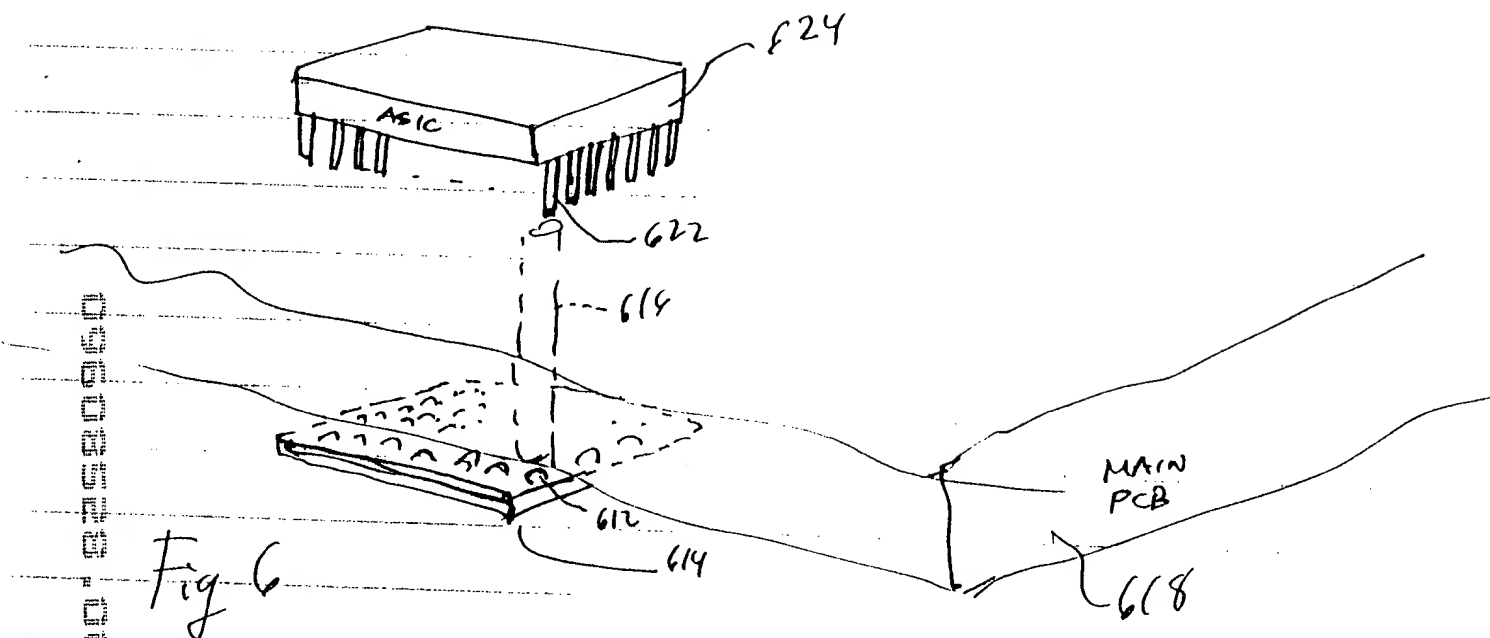


Fig 6